

AGENDA SIDDHARTA COLLABORATION MEETING
LNF 11-12 November 2005
(Directorate meeting hall)
Friday 11th November 3 p.m.

- 1) P.Lechner Status of SDD qualification
- 2) T.Frizzi,L.Bombelli Status of front-end electronics
- 3) T.Frizzi Specifications for the front-end electronics power supplies
- 4) J.Zmeskal,H.Schneider A first spectroscopic test of a 3*1 cm**2 SDD setup
- 5) J.Zmeskal,G.Corradi The final SDD ceramic layout
- 6) J.Zmeskal The SIDDHARTA setup
- 7) F.Sirghi Test of the performance of the Milano chip connected to a 5 mm**2 SDD built in the final architecture
- 8) M.Iliescu DAQ status
- 9) M.Bragadireanu Status report on SDD Bias Controller
- 10) V.Lucherini Quality test of kaon trigger PMs
- 11) M.Cargnelli How to measure X-ray lines to 1 eV precision using SDDs
- 12) T.Ishiwatari Report of SDDs response at KEK K5 beam line
- 13) A.O.I.