

## Agenda of the SIDDHARTA Collaboration Meeting

LNF, 5th July 2004  
High Energy Building, Room B1

10:30 a.m.

- 1) General information on the project  
C.Guaraldo LNF, Frascati
- 2) Mounting and bonding issues.  
Spectroscopic measurements necessary for qualification.  
Production run.  
H.Soltau PNSensor, Munich
- 3) Packaging of SDDs.  
First stage of the SIDDHARTA setup.  
H.Zmeskal IMEP, Vienna
- 4) MonteCarlo studies for the SIDDHARTA setup.  
M.Cargnelli IMEP, Vienna
- 5) Peak stability with charge sensitive preamplifier:  
results and perspectives for SIDDHARTA.  
C.Fiorini Politecnico Milano
- 6) Overview of the design of the first run of  
the front-end electronics.  
C.Fiorini Politecnico Milano
- 7) Characterization of the 30 mm\*\*2 chip:  
resolution, stability, response to background rate.  
F.Sirghi LNF, Frascati
- 8) Implementation plan.  
C.Guaraldo LNF, Frascati
- 9) A.O.I.