

# **Agenda of the SIDDHARTA Collaboration Meeting**

**LNF, Directorate meeting room**

**April 22<sup>nd</sup> 2004, 9.30 a.m.**

1. Welcome address (S. Bertolucci, Director LNF)
2. Introduction (C. Guaraldo)
3. Results of the stability tests performed at LNF on the  
7 x 5 mm<sup>2</sup> SDD chips (F. Sirghi)
4. Preliminary results for the 30 mm<sup>2</sup> chip (C. Fiorini)
5. The VLSI readout electronics for SIDDHARTA (C. Fiorini, A. Longoni)
6. DAQ architecture (M. Iliescu)
7. General considerations on setup (J. Zmeskal)
8. Preparation and realization of design and layout  
of 100 mm<sup>2</sup> SDDs for SIDDHARTA (P. Lechner)
9. Technology plan and time schedule of SIDDHARTA  
SDDs fabrication (H. Soltau)
10. SIDDHARTA Monte Carlo studies from  
Vienna (M. Cargnelli) and Frascati (C. Petrascu)
11. The SIDDHARTA trigger system (M. Bragadireanu)
12. Organizational and financial aspects.  
Implementation plan of the project. (C. Guaraldo)
13. A.O.I.